IN THE SPECIFICATION

     Please amend paragraph [0036] as marked up below:

     -- Figure 5 shows one embodiment of a parallel processor chip 500. The parallel processor chip 500 includes a parallel processor 542 and a set of registers 544. The set of registers are also referred to as the register file. The parallel processor 542 includes an encoder [[551]]531 and a decoder [[553]]533. Details of one embodiment of an encoder are discussed below with reference to Figure 7. The parallel processor writes or reads data into or from the register file 544. In one embodiment, the data stored in the register file 544 is arranged in columns and rows.--